

FIG. 8

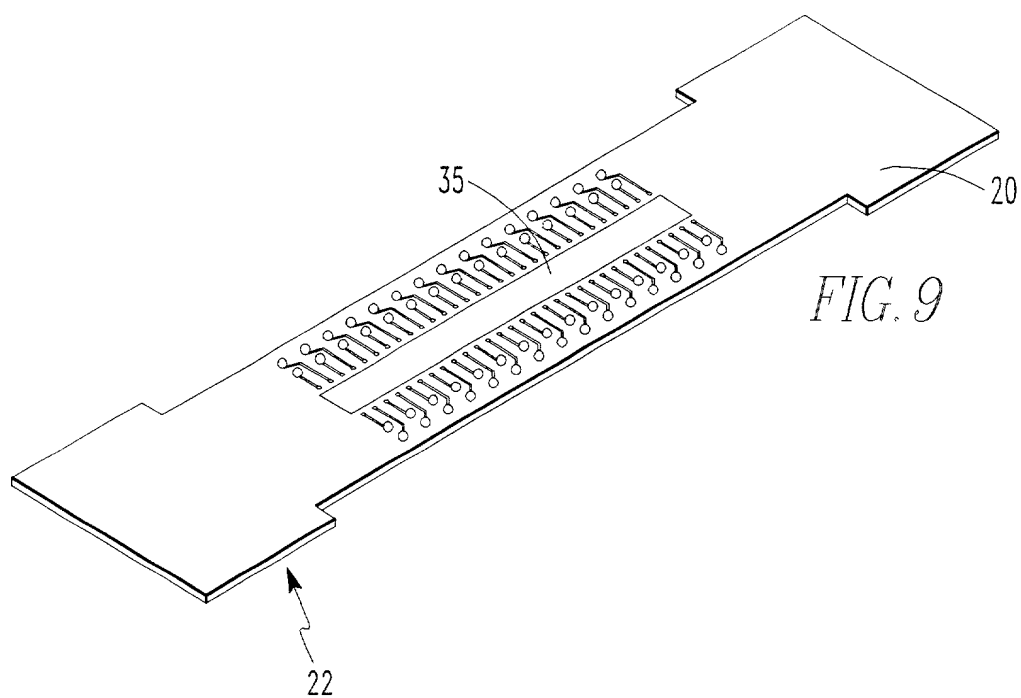
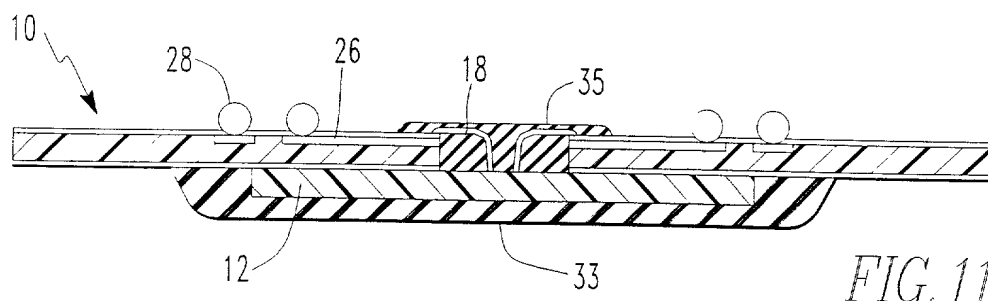
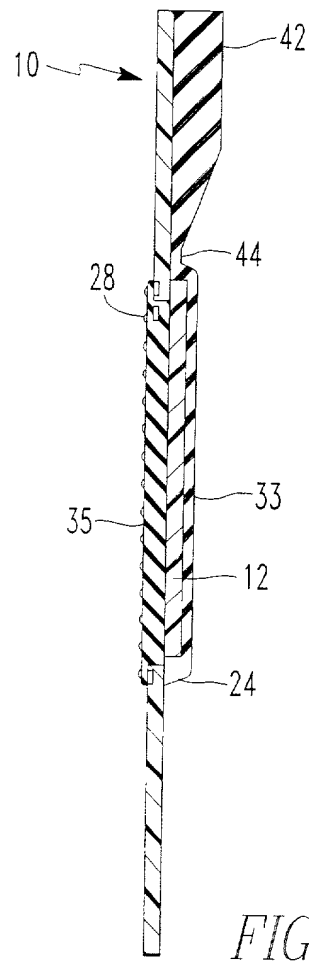
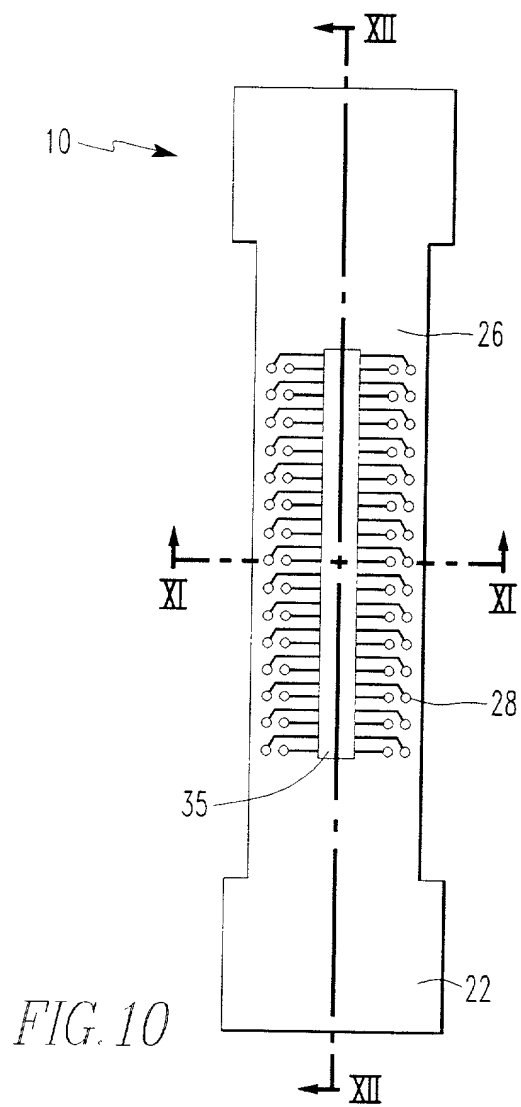


FIG. 9



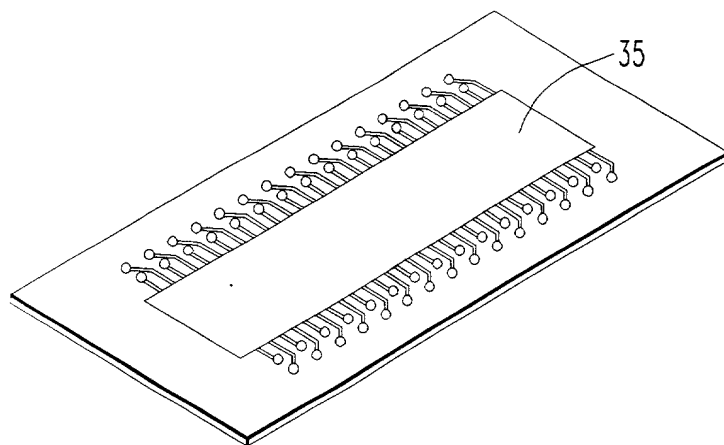


FIG. 13A

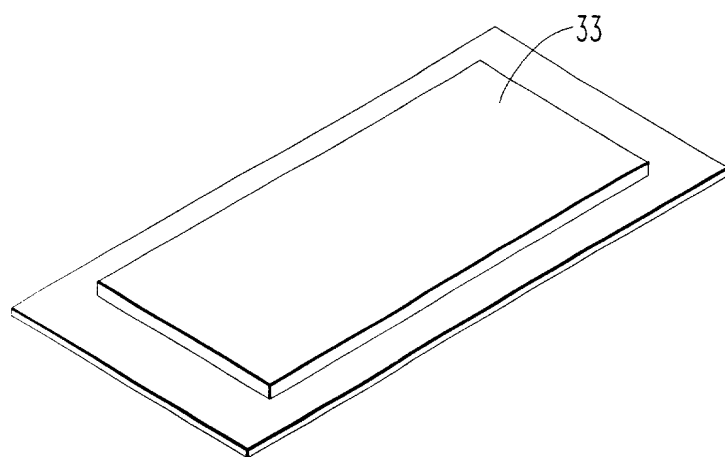


FIG. 13B

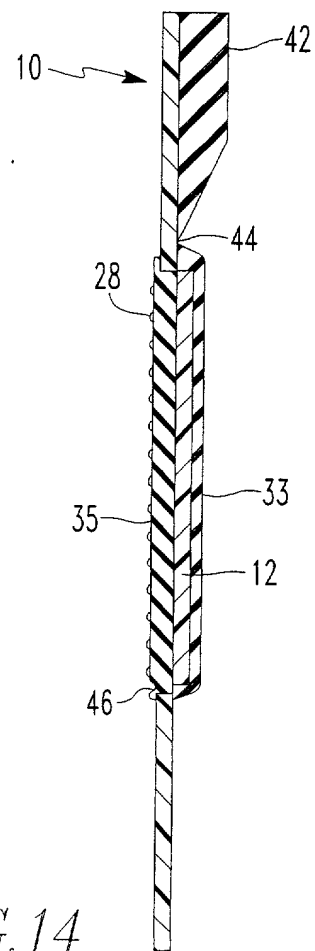


FIG. 14

FIG. 15 is a schematic diagram of a method for fabricating a semiconductor device.

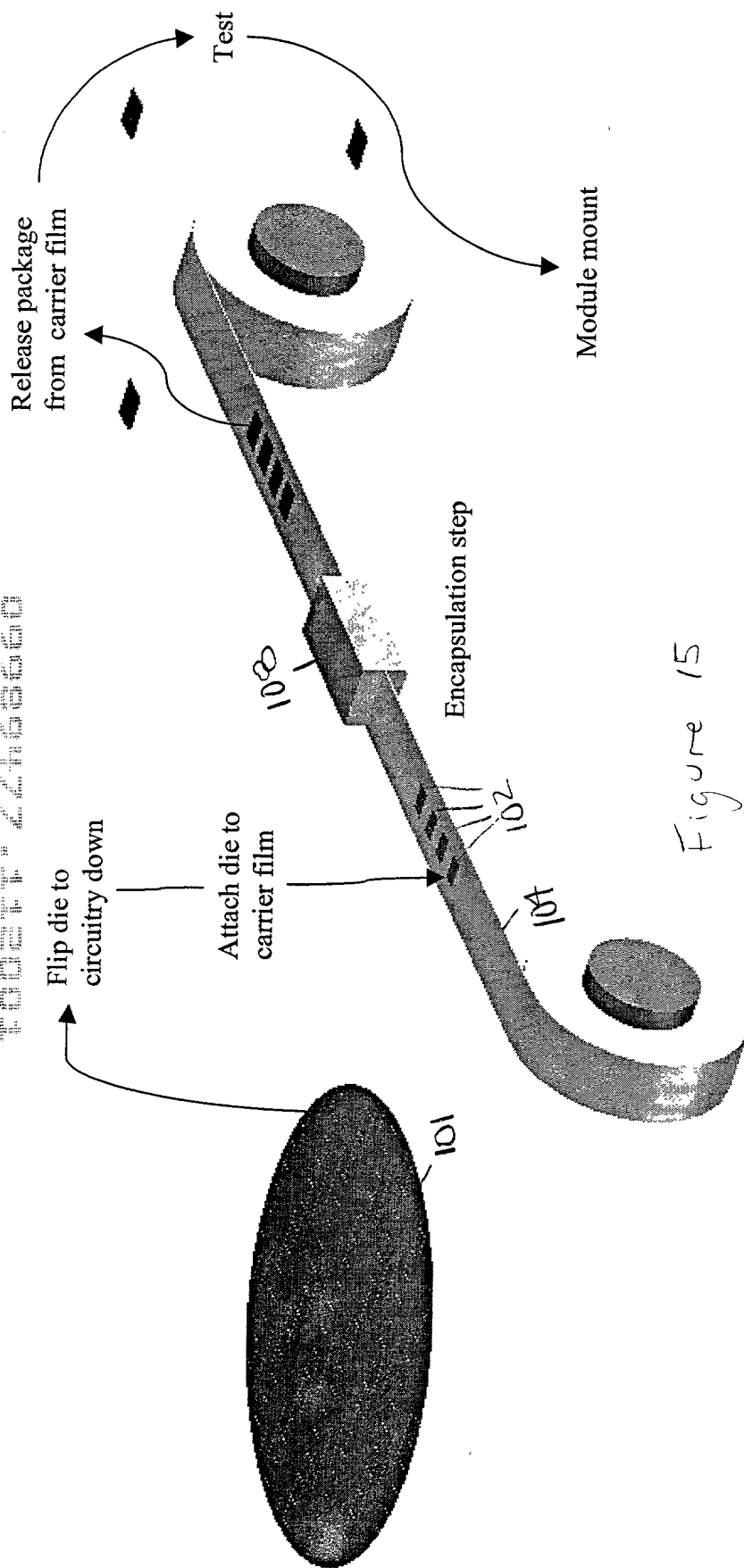


Figure 15

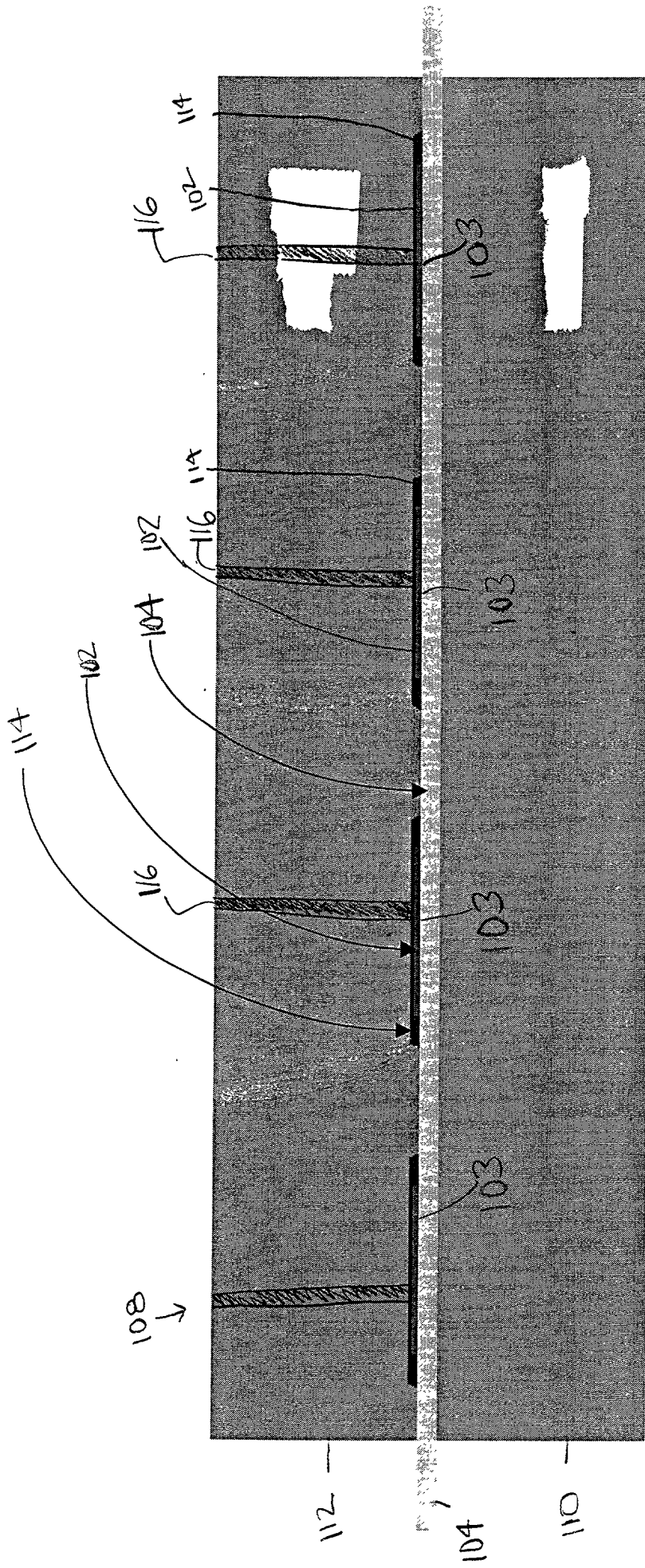


FIGURE 10



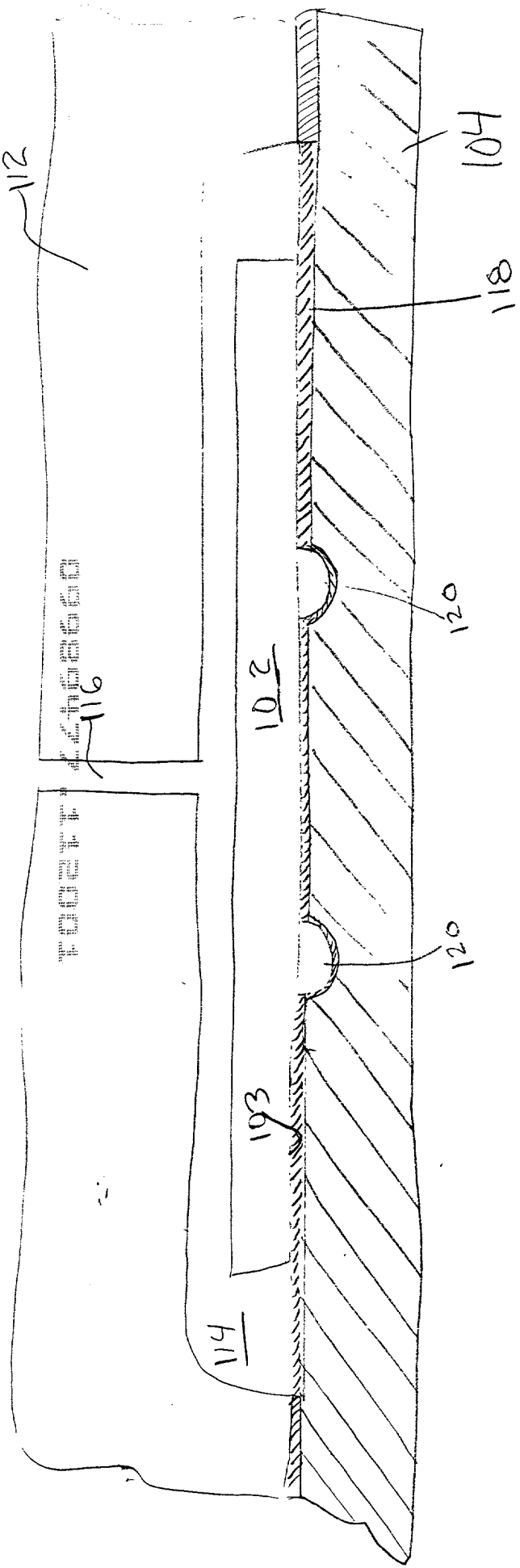


FIGURE 17

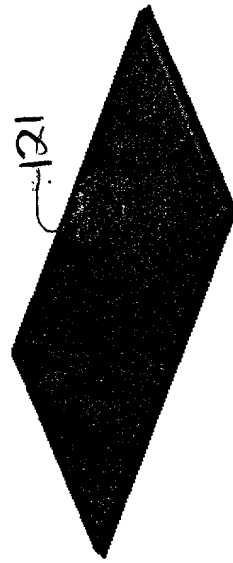


FIGURE 18A

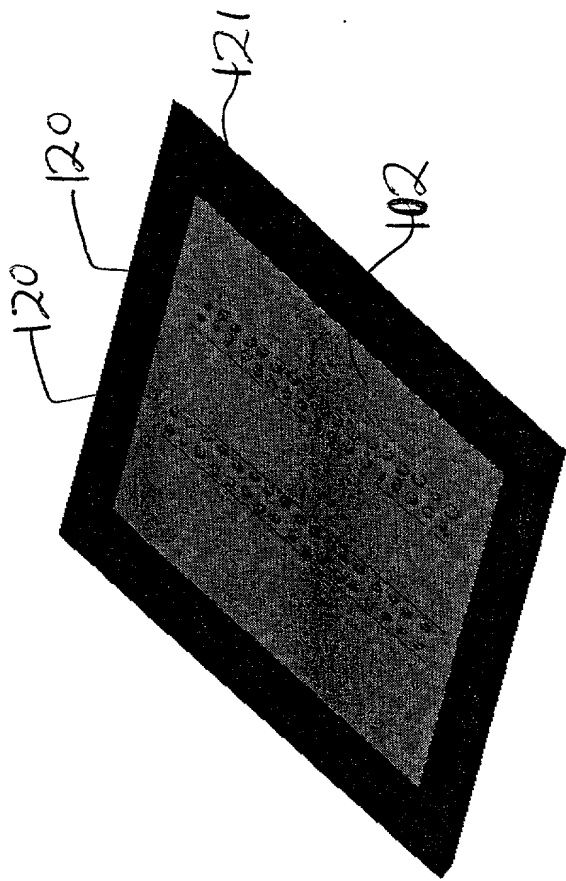


FIGURE 18B

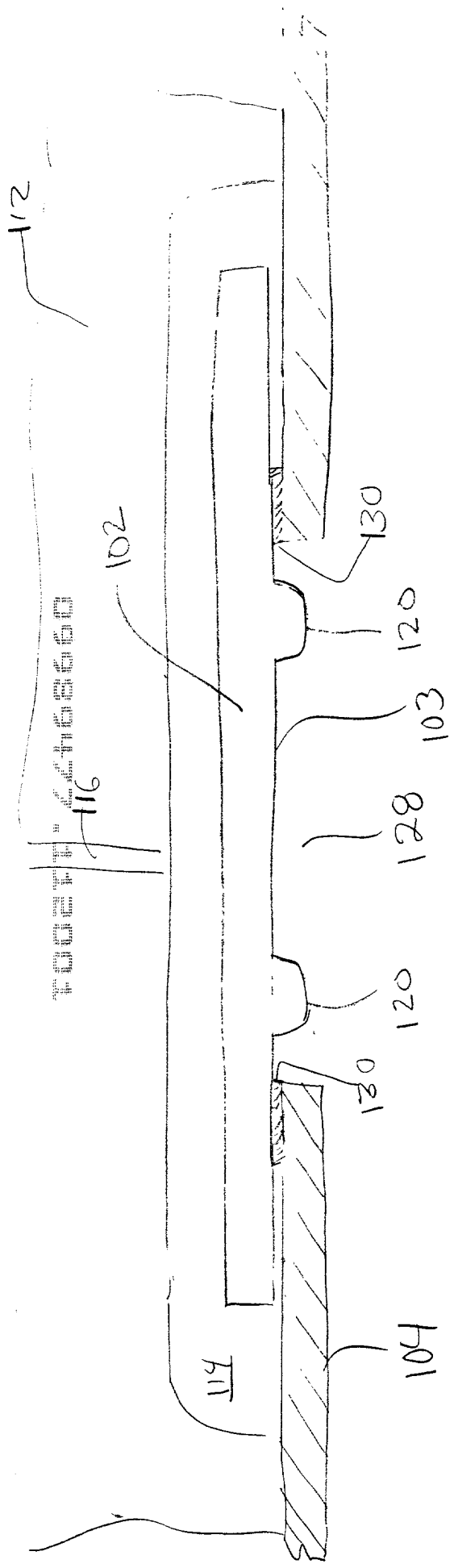


FIGURE 20

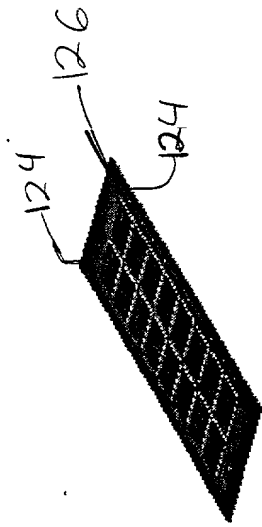


Figure 19

